

VERSION HISTORY

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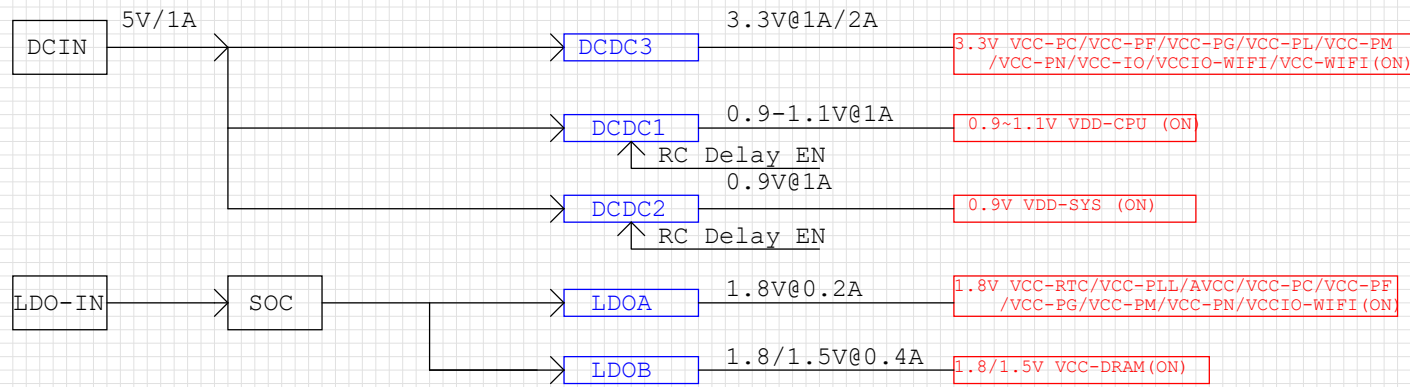
- P01 VERSION HISTORY
- P02 BLOCK DIAGRAM
- P03 POWER TREE
- P04 POWER
- P05 NGFF
- P06 SOC-SYS
- P07 SOC-GPIO
- P08 FLASH
- P09 24P FPC
- P10 WIFI/BT

| Revision | Description | Date | Drawn | Checked | Approved |
|----------|-----------------|------------|-------|---------|----------|
| Ver 1.0 | Release version | 2021-01-23 | ZK | LJJ | |
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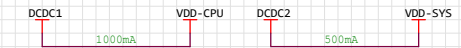
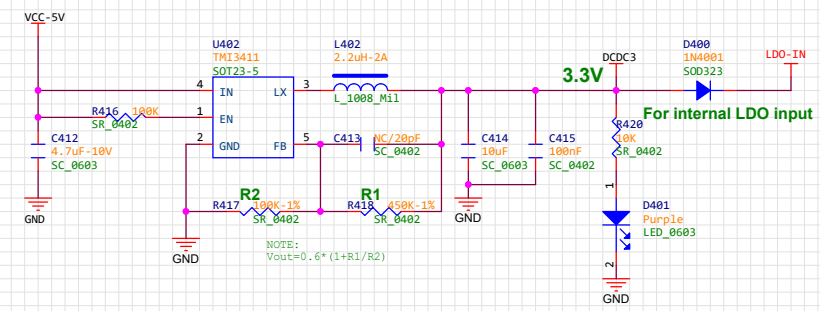
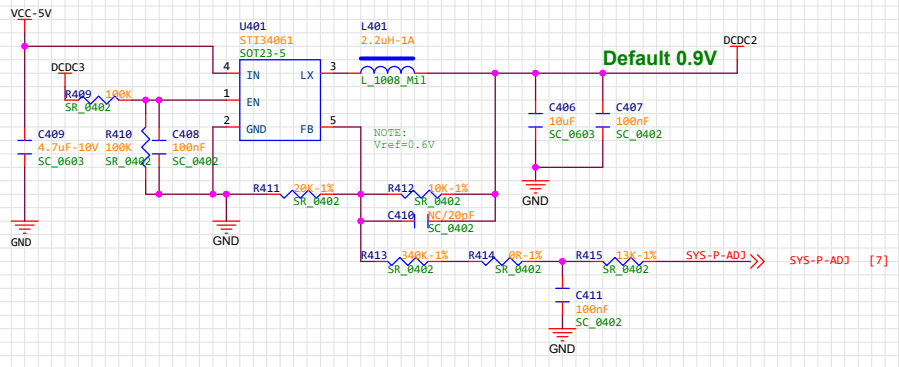
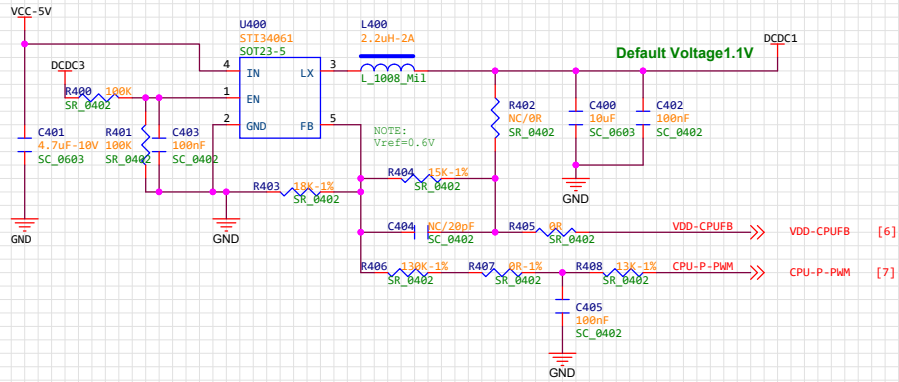
POWER TREE

DEFAULT POWER TREE
 OPTIONAL POWER TREE

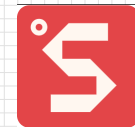
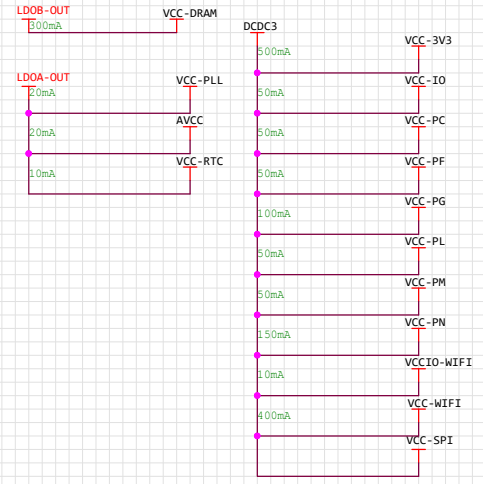


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| Sipeed Technology Co., Ltd | | | |
| Design Name: Sipeed_M2A | | | |
| Size: A3 | Page Name: POWER TREE | | Rev: 1.0 |
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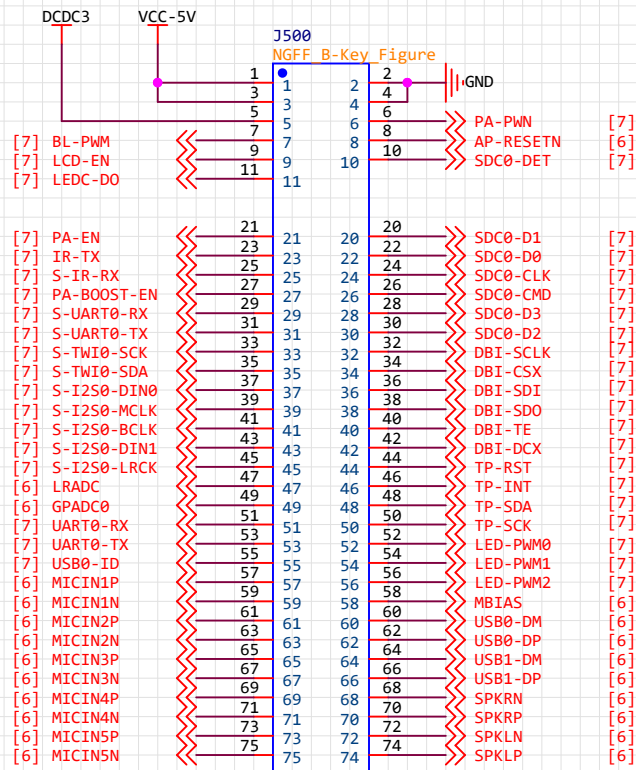
DC IN



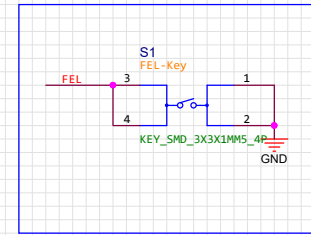
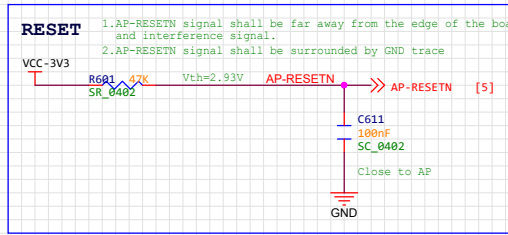
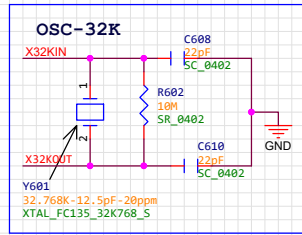
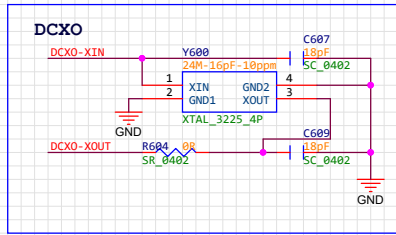
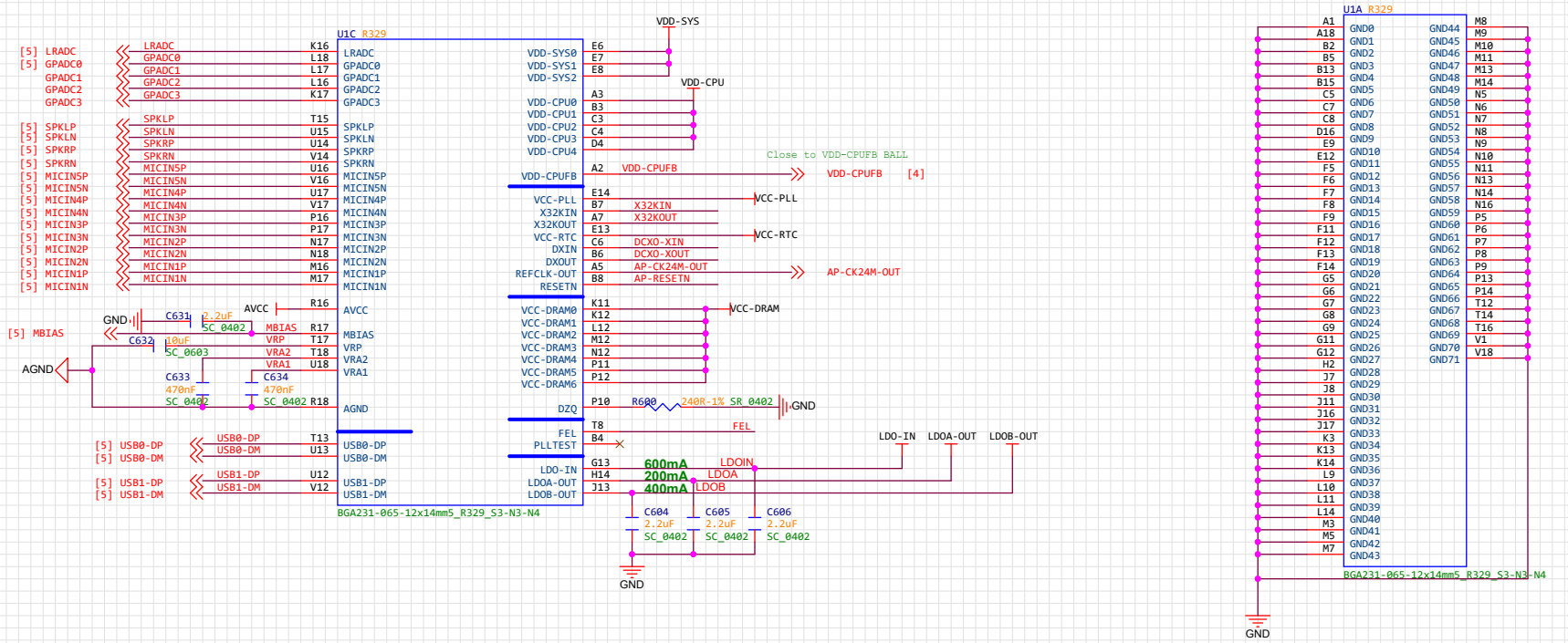
internal LDO output:LDOA-OUT/LDOB-OUT



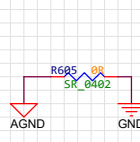
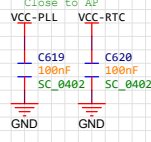
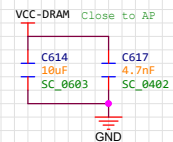
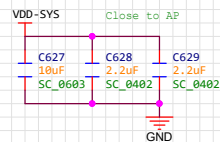
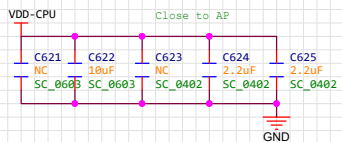
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| Design Name | | | |
| Sipeed_M2A | | | |
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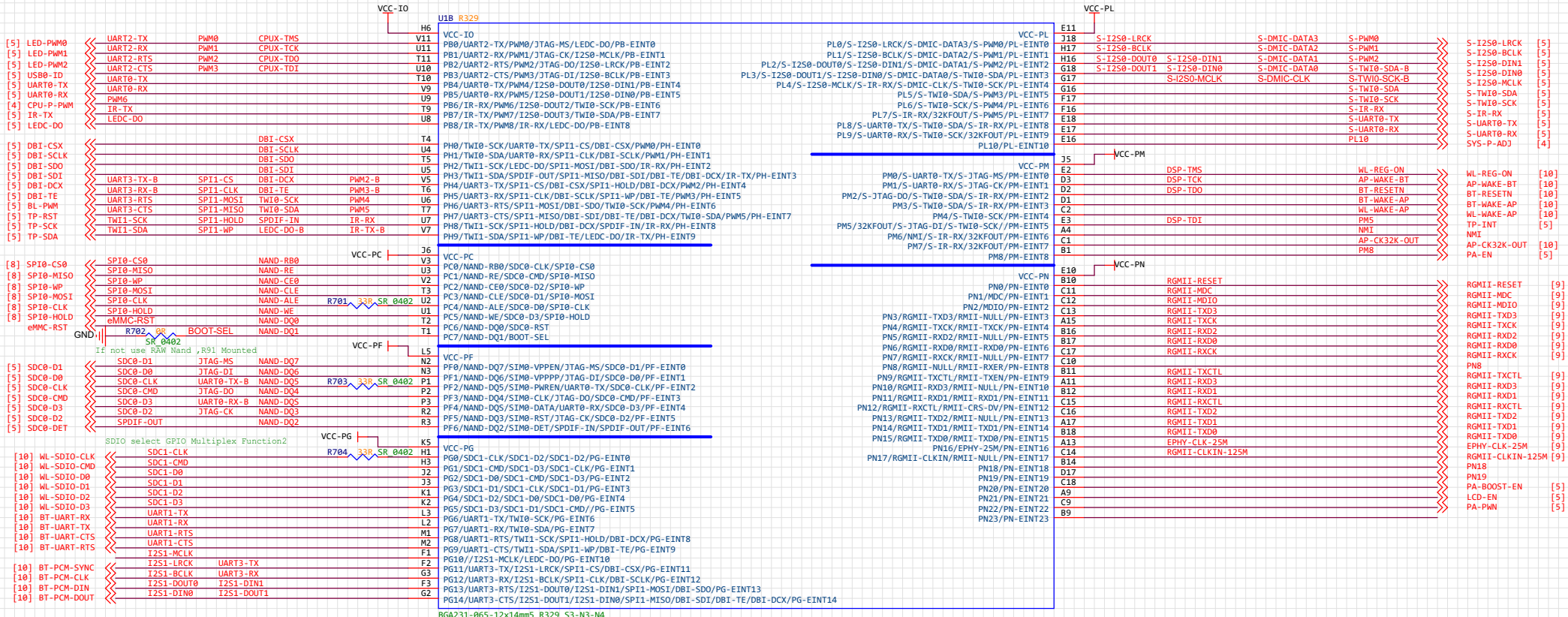
SOC-SYS



DECOUPLE CAP

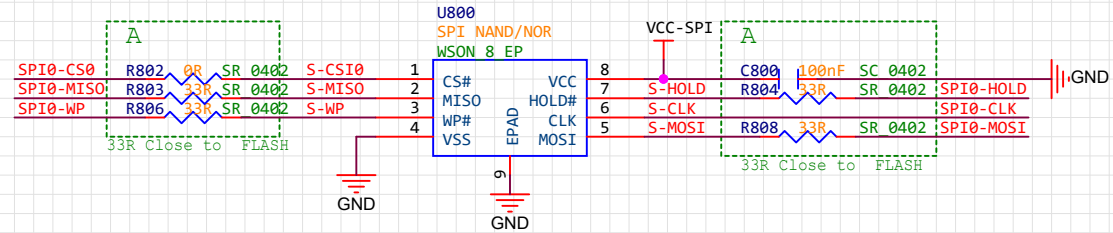


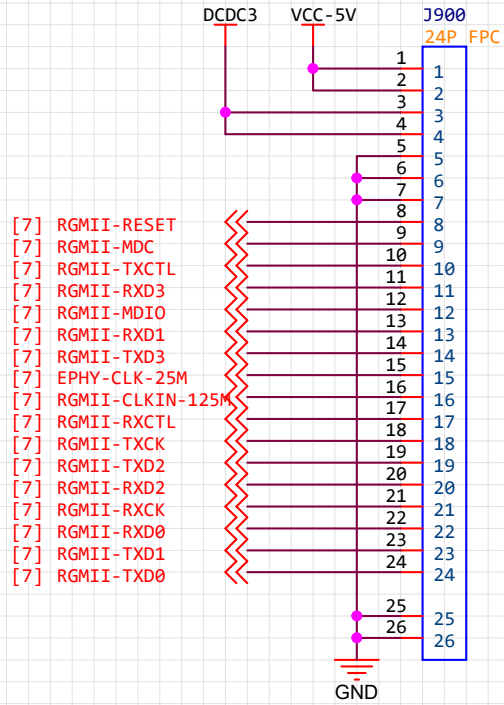
| | | |
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| Sipeed Technology Co., Ltd | | |
| Design Name: Sipeed_M2A | | |
| Size: A3 | Page Name: SOC-SYS | Rev: 1.0 |
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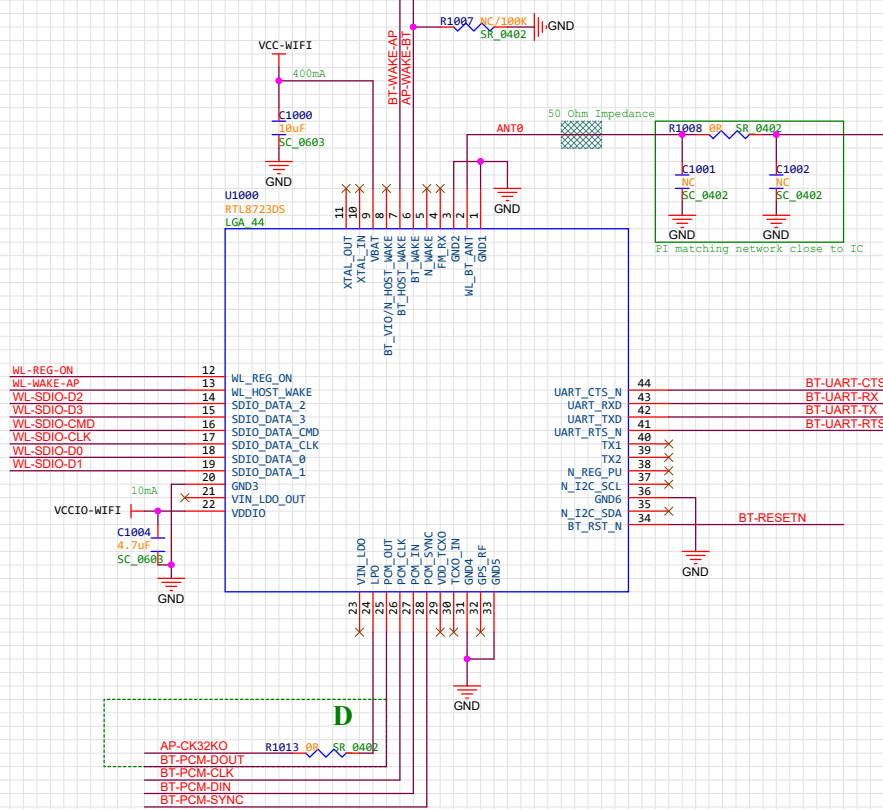
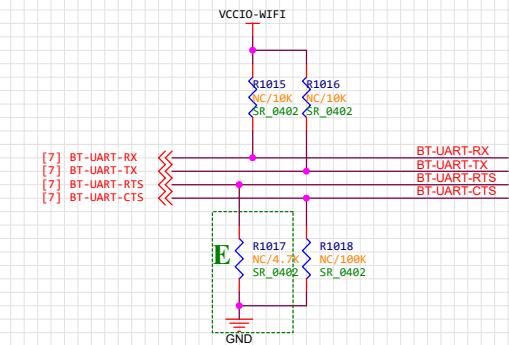
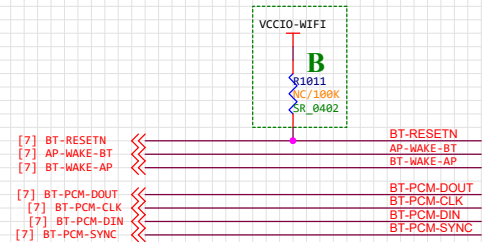
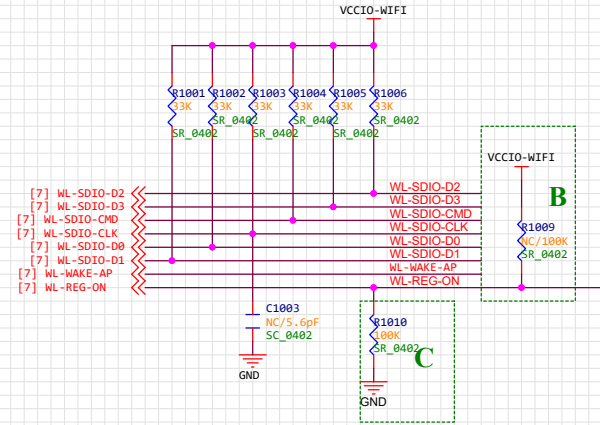
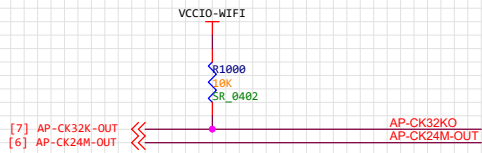
SPI NAND/NOR

| | | |
|---------------|-----------|----------|
| [7] SPI0-CS0 | SPI0-CS0 | eMMC-CLK |
| [7] SPI0-MISO | SPI0-MISO | eMMC-CMD |
| [7] SPI0-WP | SPI0-WP | eMMC-D2 |
| [7] SPI0-MOSI | SPI0-MOSI | eMMC-D1 |
| [7] SPI0-CLK | SPI0-CLK | eMMC-D0 |
| [7] SPI0-HOLD | SPI0-HOLD | eMMC-D3 |
| [7] eMMC-RST | | eMMC-RST |

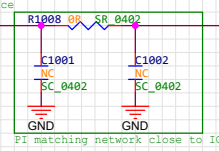




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| Design Name | | |
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The surrounding area of ANT needs to be cleared.



| NOTE | ANT | A | B | C | D | E |
|------------------|--------|-------|-------|-------|-------|-------|
| XR829 Module | single | NC | NC | Mount | Mount | NC |
| RTL8723DS Module | single | NC | Mount | NC | NC | NC |
| RTL8723DS Module | double | Mount | Mount | NC | NC | NC |
| RTL8723FS Module | double | Mount | Mount | NC | NC | NC |
| RTL8733BS Module | double | Mount | Mount | NC | NC | NC |
| RTL821CS Module | single | NC | Mount | NC | NC | Mount |
| RDA5622 Module | single | NC | NC | Mount | Mount | NC |

